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PATENT  
Customer Number 22,852  
Attorney Docket No. 04329.2270-01000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Shinsuke SAKAMOTO et al. ) Group Art Unit: 2815  
Application No.: 10/081,109 ) Examiner: L. Eugene  
Filed: February 25, 2002 ) Confirmation No.: 4805  
For: SEMICONDUCTOR ) Allowed: March 29, 2004  
INTEGRATED CIRCUIT DEVICE )  
AND WIRING ARRANGING )  
METHOD THEREOF )  
)

**Attn: Office of the Assistant Commissioner for Patents**

P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PETITION FOR DEFERRAL OF ISSUANCE OF PATENT**

Pursuant to 37 C.F.R. § 1.314, Applicants hereby respectfully petition the Commissioner to grant a deferral in the assignment of a patent number and issue date for the referenced allowed United States patent application. According to the fee schedule set forth in 37 C.F.R. § 1.17(h) the required fee of \$130.00 for consideration of this Petition is enclosed herewith.

This request for a deferred issuance of a prospective United States patent is made for the causes set forth below which it is respectfully submitted constitute good and sufficient reasons for the action sought. Attached is a copy of the Issue Fee Transmittal form.

On March 29, 2004, the Examiner issued a Notice of Allowance and required Applicants to submit corrected drawings (as "replacement sheets"). In response, on

07/09/2004 AADDF01 00000032 10081109  
01 FC:1460 130.00 OP

Customer Number 22,852  
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June 28, 2004, Applicants submitted a submission of replacement drawings for Figs. 1-4 and paid the issue fee.

Upon further review, Applicants noticed that the replacement drawing sheets submitted on June 28, 2004, did not include some of the changes as set forth in Applicant's Request for Approval of Drawing Change dated February 19, 2004. Specifically, amendments directed to changing certain broken lines into solid lines and certain solid lines into broken lines on Fig. 2 were not included on the replacement drawing sheets submitted on June 28, 2004. In particular, lines of vias 15, 18, 23, and 32 were corrected from a solid line to a broken line or vice versa to properly represent portions that are visible and invisible. Applicants also submit a Supplemental Submission of Replacement Sheets herewith.

Applicants respectfully request deferral of issuance of the above-captioned case for a period of time in order for the Examiner to consider, approve, and incorporate the supplemental replacement sheets into the patent to be issued.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: July 6, 2004

By: Richard V. Burgujian #40266  
for Richard V. Burgujian  
Reg. No. 31,744

## PART B - FEE(S) TRANSMITT

Complete and send this form, together w. applicable fee(s), to: Mail

JUL 06 2004

**COPY**  
**Mail Stop ISS, FEE  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 (703) 746-4000**

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**INSTRUCTIONS:** This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 4 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Legibly mark-up with any corrections or use Block 1)

22852 7590 03/29/2004

**FINNEGAN, HENDERSON, FARABOW, GARRETT &  
 DUNNER  
 LLP  
 1300 I STREET, NW  
 WASHINGTON, DC 20005**

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

**Certificate of Mailing or Transmission**

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO, on the date indicated below.

(Depositor's name)

(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/081,109	02/25/2002	Shinsuke Sakamoto	4329.2270-01	4805

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND WIRING ARRANGING METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/29/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
LEE, EUGENE	2815		257-776000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 FINNEGAN, HENDERSON,  
 2 FARABOW, GARRETT &  
 3 DUNNER, L.L.P.

## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

KABUSHIKI KAISHA TOSHIBA

Kawasaki-shi, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent):  individual  corporation or other private group entity  government

4a. The following fee(s) are enclosed:

4b. Payment of Fee(s):

 Issue Fee A check in the amount of the fee(s) is enclosed. Publication Fee Payment by credit card. Form PTO-2038 is attached. Advance Order - # of Copies 7 (\$21)

The Director is hereby authorized to charge the required fee(s), or credit any overpayment, to Deposit Account Number 06-0916 (enclose an extra copy of this form).

Director for Patents is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

EBNEST F. CHAPMAN

(Authorized Signature)

Reg. No. 25,961 (Date)

JUN 28 2004

Richard V. Burgulian, Reg. No. 31,744

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Alexandria, Virginia 22313-1450.

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For: SEMICONDUCTOR INTEGRATED ) Examiner: Lee Eugene  
CIRCUIT DEVICE AND WIRING )  
ARRANGING METHOD THEREOF )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**SUPPLEMENTAL SUBMISSION OF REPLACEMENT DRAWINGS**

Filed herewith are 2 sheets of supplemental replacement patent drawings containing Figures 1-4. It is requested that this drawings be made of official record in the above-identified patent application. Applicants submit this paper concurrently with a Petition of Deferral of Issuance of Patent.

Applicants previously submitted replacement drawings on June 28, 2004. Upon further review, Applicants noticed that some amendments outlined for Fig. 2 in the request for approval of drawing change dated February 19, 2004, did not appear in the replacement drawing sheets submitted on June 28, 2004. Applicants submit the present supplemental replacement drawing sheets in order to correct the previously submitted drawing sheets. Specifically, with regard to Fig. 2, certain broken lines should appear as solid lines and vice versa in order to properly represent that the lines

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are either visible or invisible. Applicants respectfully request the Examiner to accept the supplemental drawing sheets so that they may be incorporated into the patent.

If there are any fees due in connection with the filing of this drawing, please charge the fees to our Deposit Account No. 06-0916. If a fee is required for an extension of time under 37 C.F.R. 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: July 6, 2004

By: Charles I Van Thon #140266  
*for* Richard V. Burgujian  
Reg. No. 31,744